

## Form - 1A : SLO Compliance Log Computer Hardware

(to be submitted by author/publisher/applicant along with the manuscript)

**GRADE – 10**

Publisher Name: \_\_\_\_\_

Textbook Title and Grade \_\_\_\_\_

Author Name/Signature \_\_\_\_\_

Date: \_\_\_\_\_

Please check ✓ the relevant box to record your textbook's compliance with the Progression Grid SLOs.

SLO Code (Progression Grid)	Covered Directly	Covered Indirectly (e.g., covered implicitly in content, covered through diagrams/ graphics, tables, activities, or assessments in textbooks)	Covered in Addendum	Not Covered	Reference (Pg. No. ;Line No.)
<b>Domain A</b>	<b>Computer System (Hardware and Software)</b>				
[SLO: CHW-10-A-01]					
[SLO: CHW-10-A-02]					
[SLO: CHW-10-A-03]					
[SLO: CHW-10-A-04]					
<b>Domain B</b>	<b>Number System, Boolean Logic, Gates and Transistors</b>				
[SLO: CHW-10-B-01]					
[SLO: CHW-10-B-02]					
[SLO: CHW-10-B-03]					
[SLO: CHW-10-B-04]					
[SLO: CHW-10-B-05]					
<b>Domain C</b>	<b>Computer Architecture</b>				
[SLO: CHW-10-C-01]					

[SLO: CHW-10-C-02]						
[SLO: CHW-10-C-03]						
[SLO: CHW-10-C-04]						
<b>Domain D</b>	<b>Memory</b>					
[SLO: CHW-10-D-01]						
[SLO: CHW-10-D-02]						
[SLO: CHW-10-D-03]						
[SLO: CHW-10-D-04]						
<b>Domain E</b>	<b>Input/Output Device</b>					
[SLO:CHW-10-E-01]						
[SLO:CHW-10-E-02]						
[SLO:CHW-10-E-03]						
[SLO:CHW-10-E-04]						
<b>Domain F</b>	<b>Networking Devices</b>					
[SLO:CHW-10-F-01]						
[SLO:CHW-10-F-02]						
[SLO:CHW-10-F-03]						
[SLO:CHW-10-F-04]						
<b>Domain G</b>	<b>Emerging Trends in Computer Hardware</b>					
[SLO:CHW-10-G-01]						
[SLO:CHW-10-G-02]						
<b>Domain H</b>	<b>Design Project - Build your own Computer</b>					
[SLO:CHW-10-H-01]						
[SLO:CHW-10-H-02]						
[SLO:CHW-10-H-03]						